

```

=> s 395/?/ccls
L1      26767 395/?/CCLS

=> s l1 and processor? and dma and stack? and (loop (1a) counter)
      83632 PROCESSOR?
      5958 DMA
      131071 STACK?
      174359 LOOP
      211010 COUNTER
      1103 LOOP (1A) COUNTER
L2      °      45 L1 AND PROCESSOR? AND DMA AND STACK? AND (LOOP (1A) COUNTER)

=> s l2 and (stack (2a) pointer) and (mode (1a) register)
      74331 STACK
      24012 POINTER
      1498 STACK (2A) POINTER
      350031 MODE
      111317 REGISTER
      1872 MODE (1A) REGISTER
L3      5 L2 AND (STACK (2A) POINTER) AND (MODE (1A) REGISTER)

=>

```

INPUT: